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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/813,907	03/30/2004	Christopher J. Diorio	IMPJ-0027A	5046
49684	7590	05/31/2007	EXAMINER	
IMPJ - THELEN REID BROWN RAYSMAN & STEINER LLP P.O. BOX 640640 SAN JOSE, CA 95164-0640			PHAN, TRONG Q	
		ART UNIT	PAPER NUMBER	
		2827		
		MAIL DATE	DELIVERY MODE	
		05/31/2007	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

6/1

Response to Rule 312 Communication	Application No.	Applicant(s)	
	10/813,907	DIORIO ET AL.	
	Examiner	Art Unit	
	TRONG PHAN	2827	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

1. The amendment filed on 15 May 2007 under 37 CFR 1.312 has been considered, and has been:

- a) entered.
- b) entered as directed to matters of form not affecting the scope of the invention.
- c) disapproved because the amendment was filed after the payment of the issue fee.
Any amendment filed after the date the issue fee is paid must be accompanied by a petition under 37 CFR 1.313(c)(1) and the required fee to withdraw the application from issue.
- d) disapproved. See explanation below.
- e) entered in part. See explanation below.

Phan Trong

TRONG PHAN
PRIMARY EXAMINER

REPLACEMENT SHEET

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Approved
TP
5/22/07

10 ↗

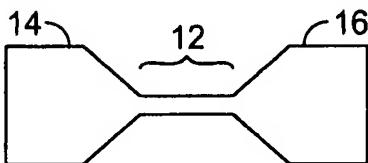


FIG. 1
(Prior Art)

20 ↗

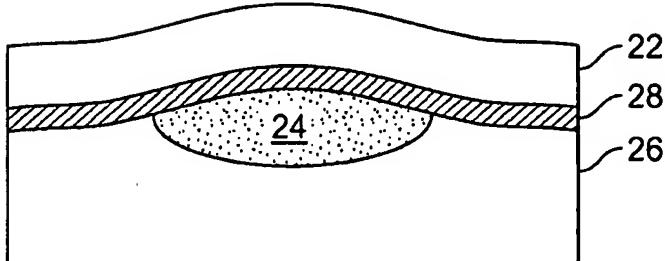


FIG. 2
(Prior Art)

30 ↗

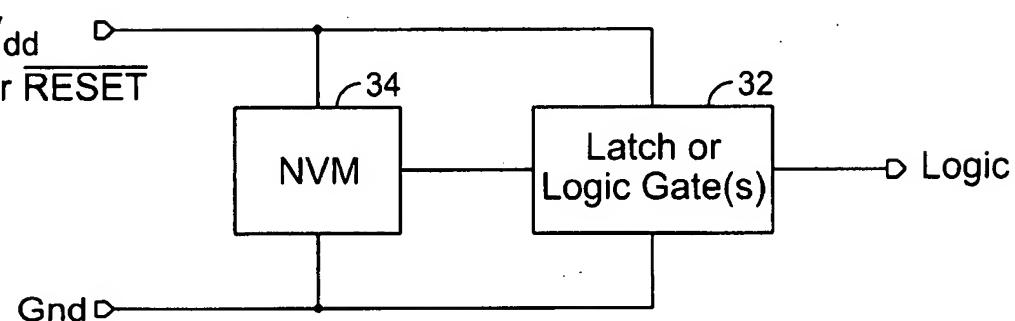


FIG. 3



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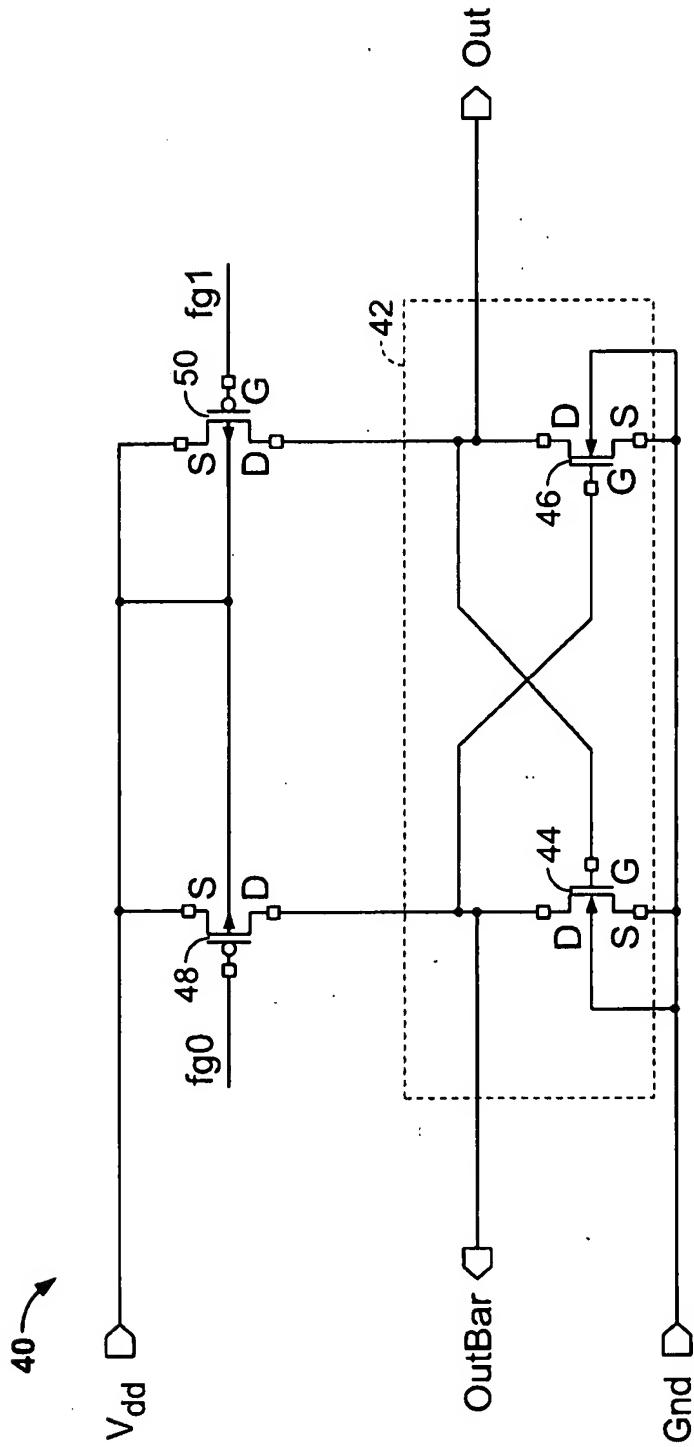


FIG. 4

REPLACEMENT SHEET

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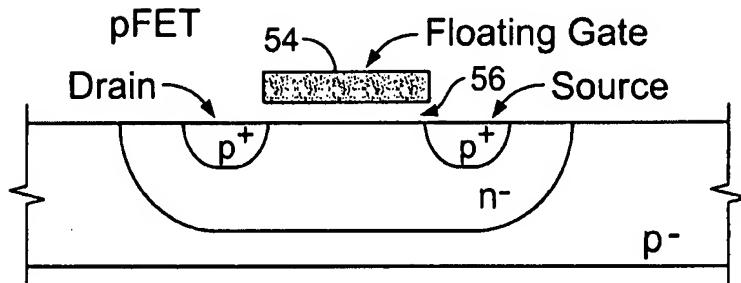


FIG. 5A

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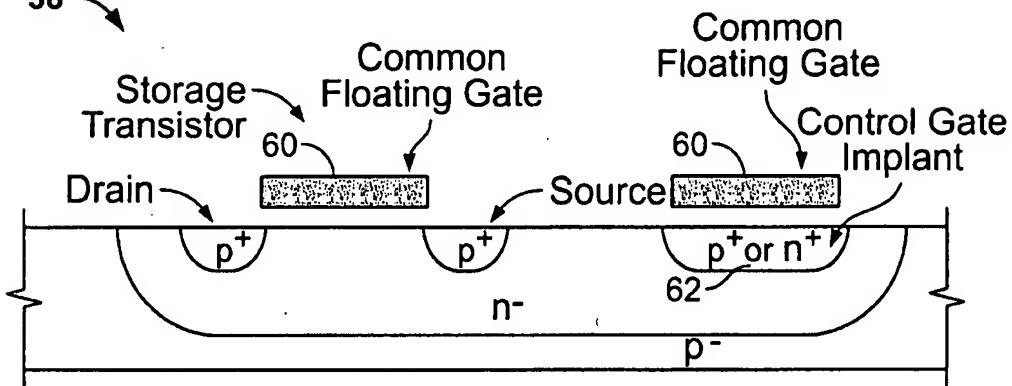


FIG. 5B

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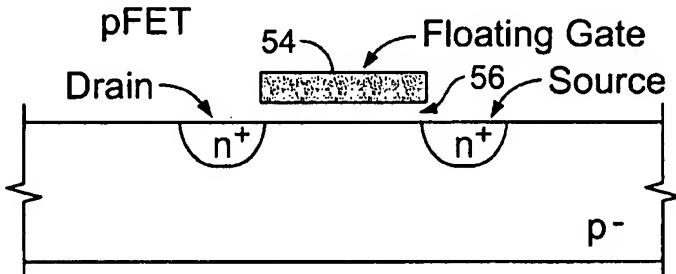


FIG. 5C

REPLACEMENT SHEET

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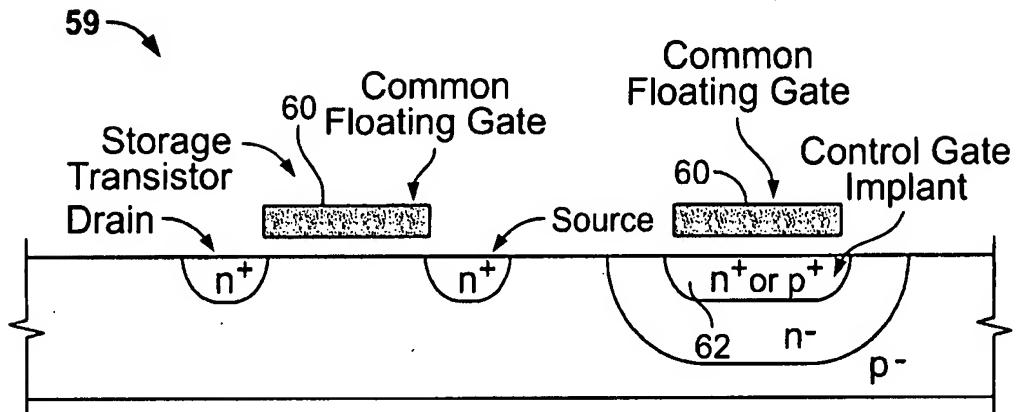


FIG. 5D

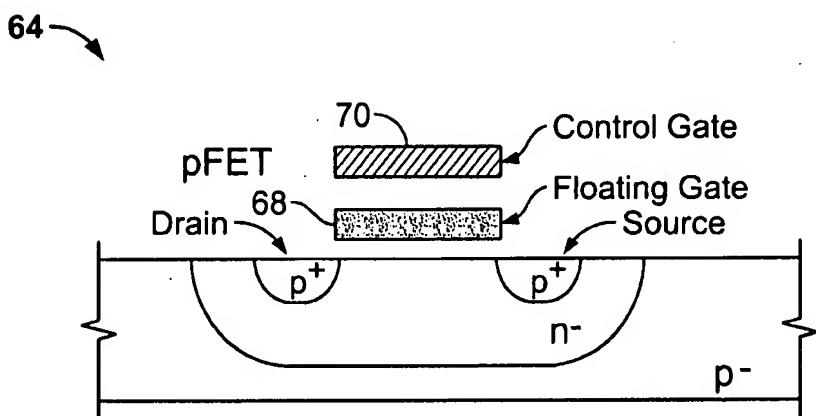


FIG. 5E

REPLACEMENT SHEET

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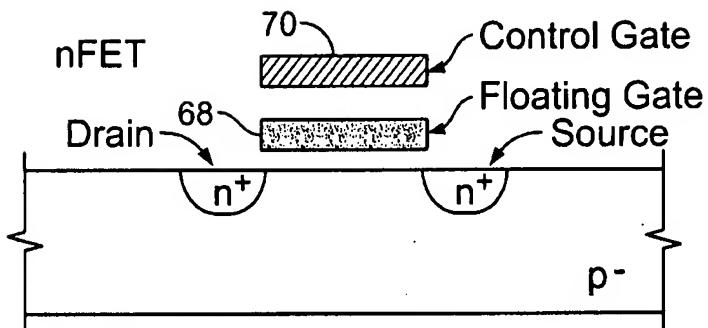


FIG. 5F

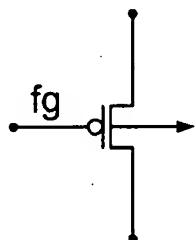


FIG. 6

REPLACEMENT SHEET

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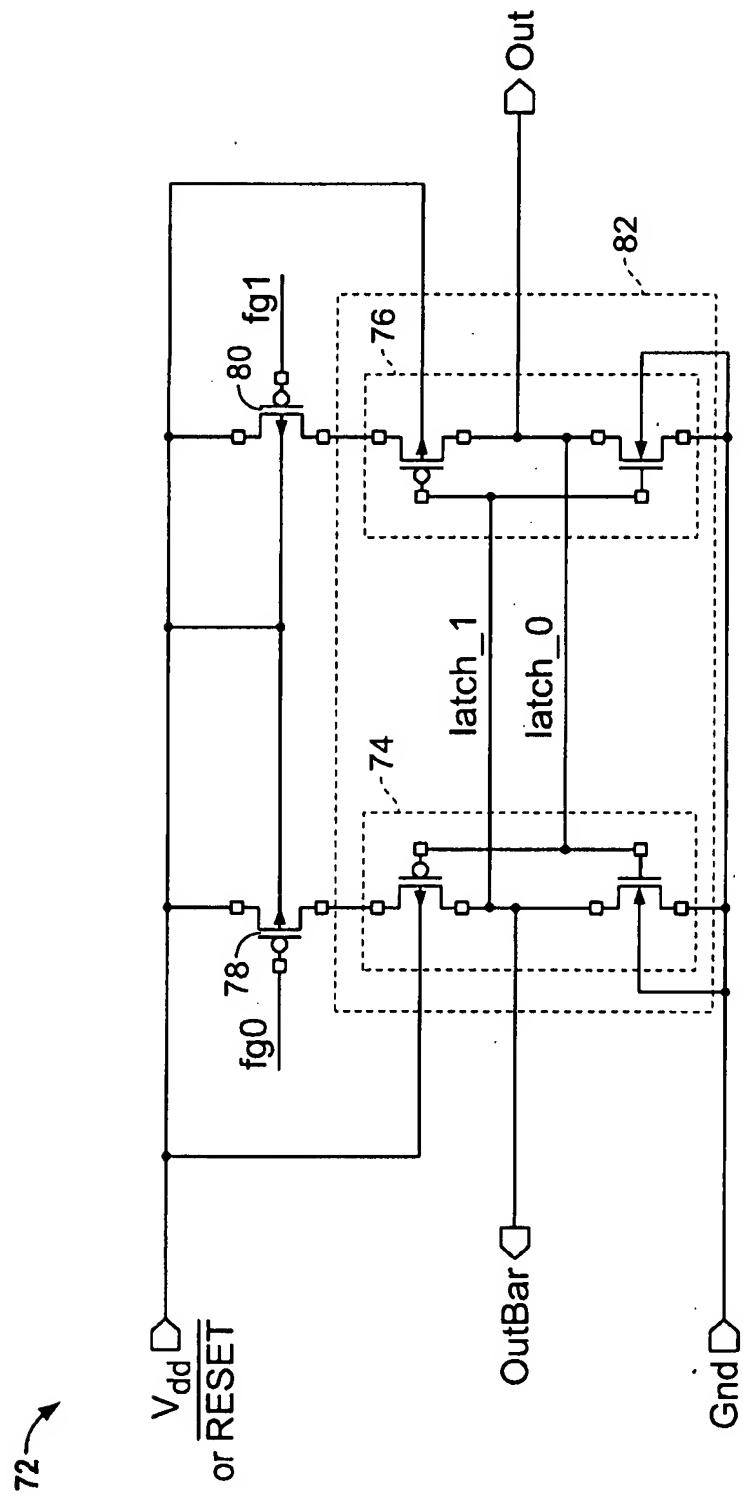


FIG. 7

REPLACEMENT SHEET

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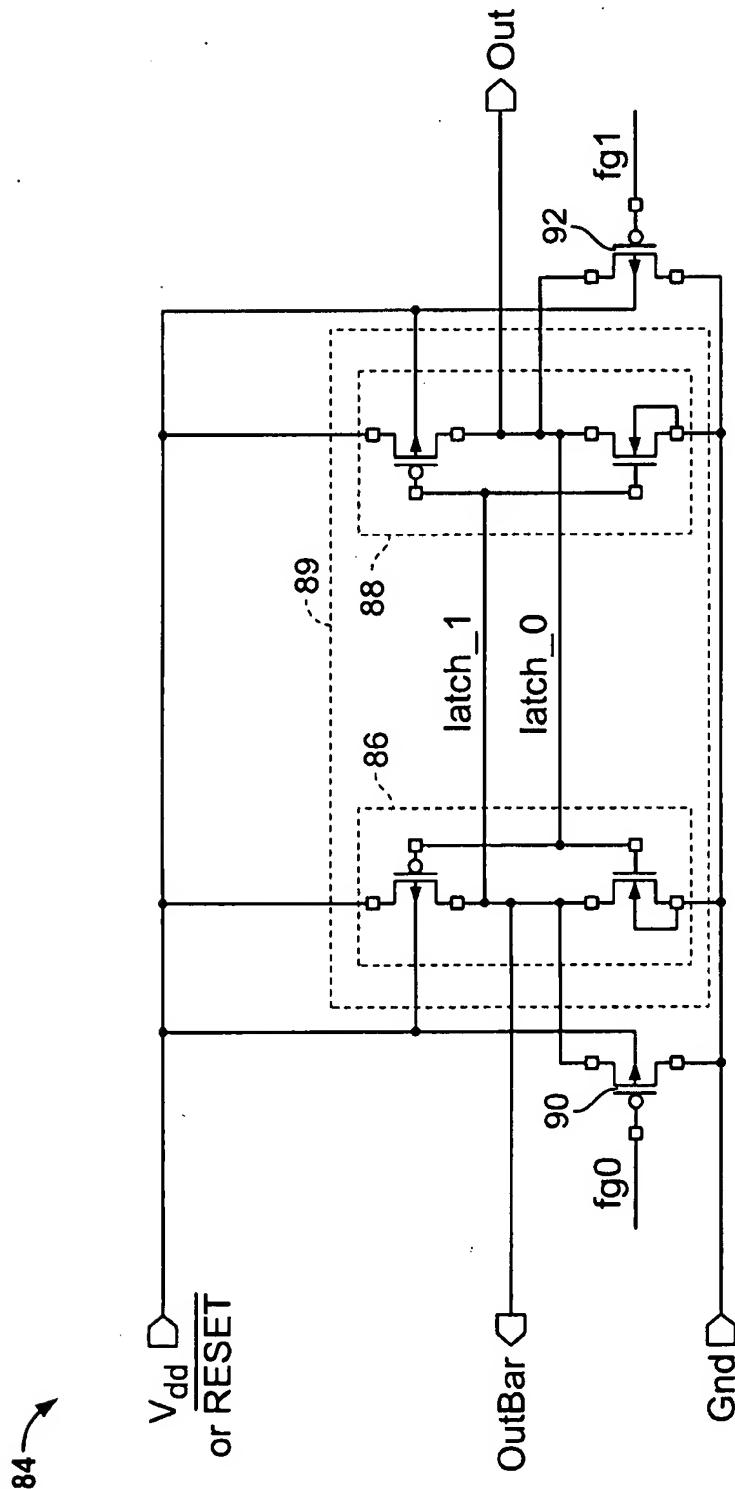


FIG. 8

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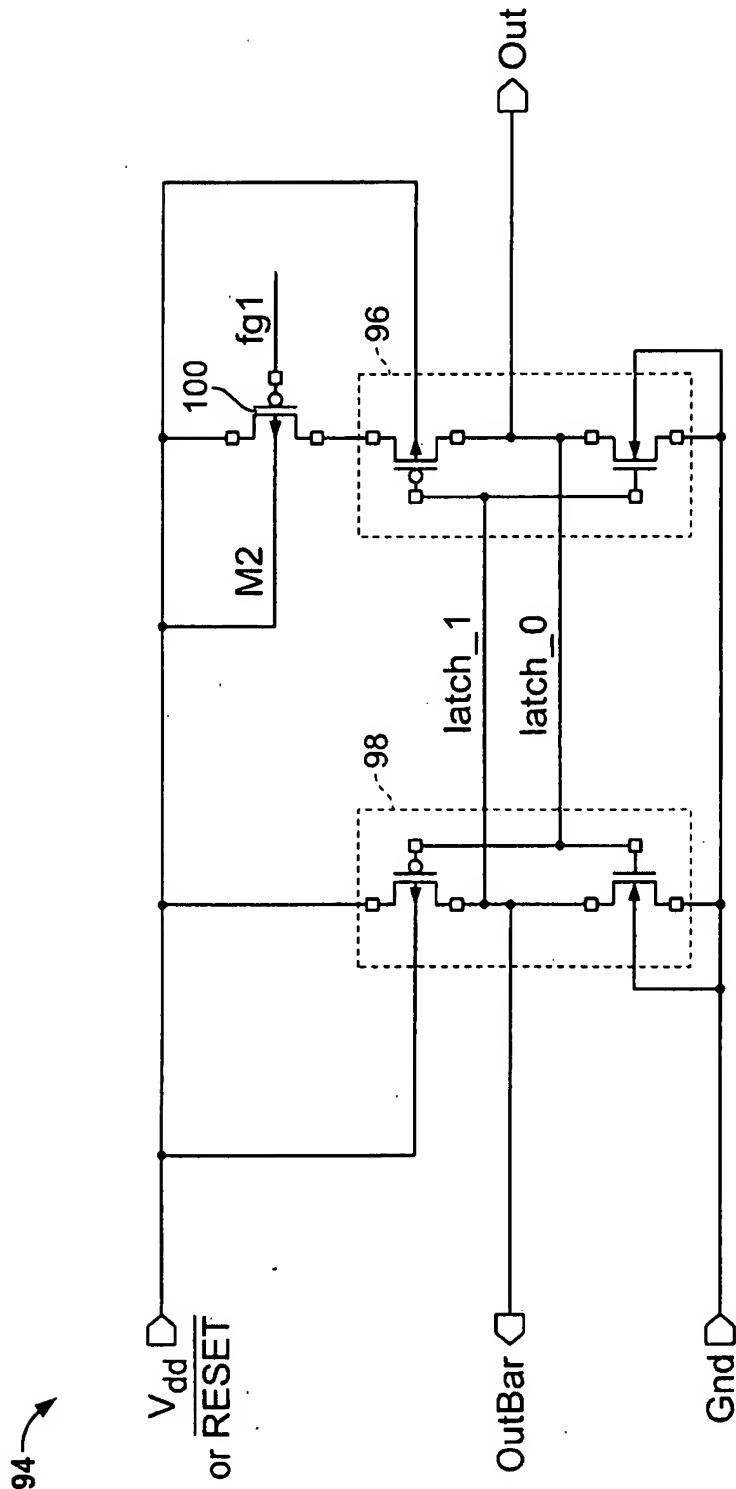


FIG. 9

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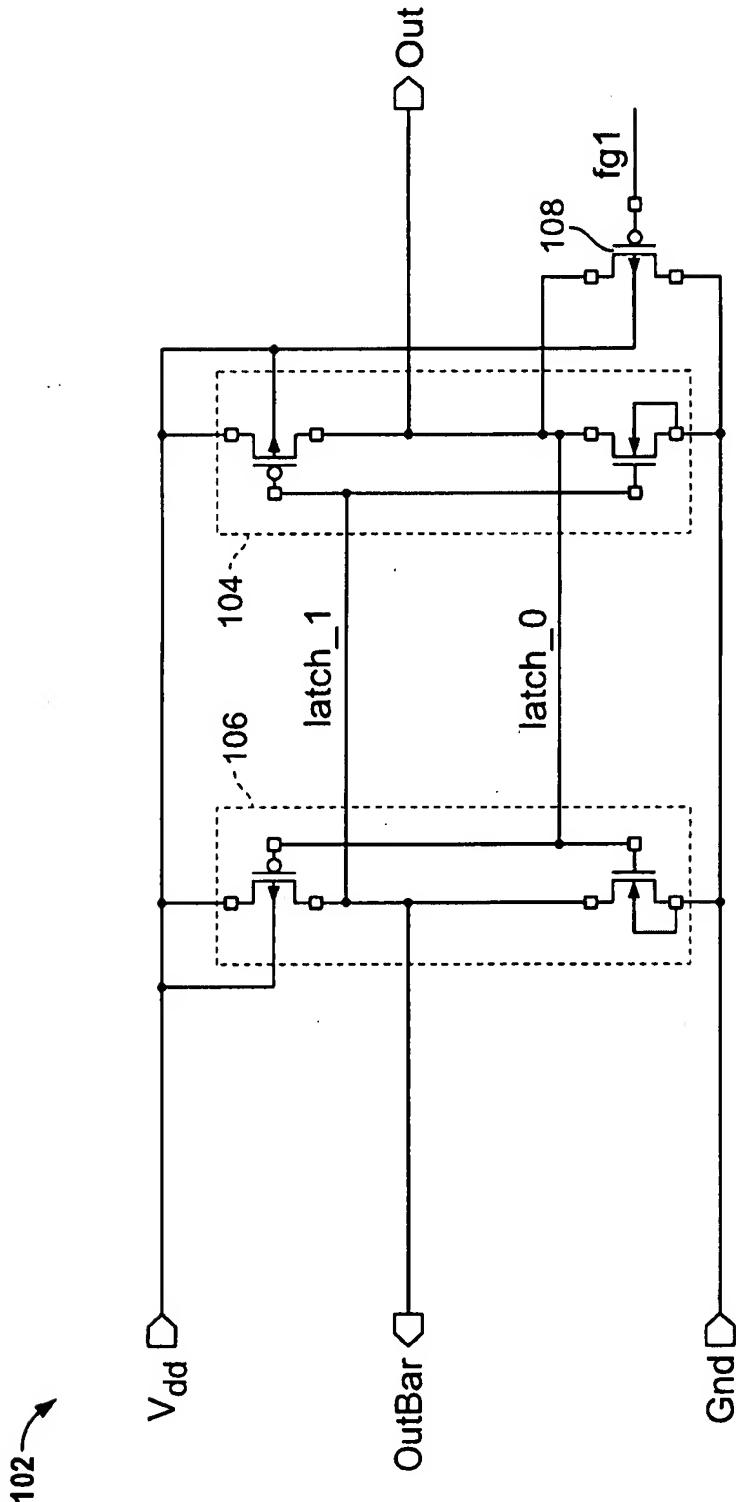


FIG. 10

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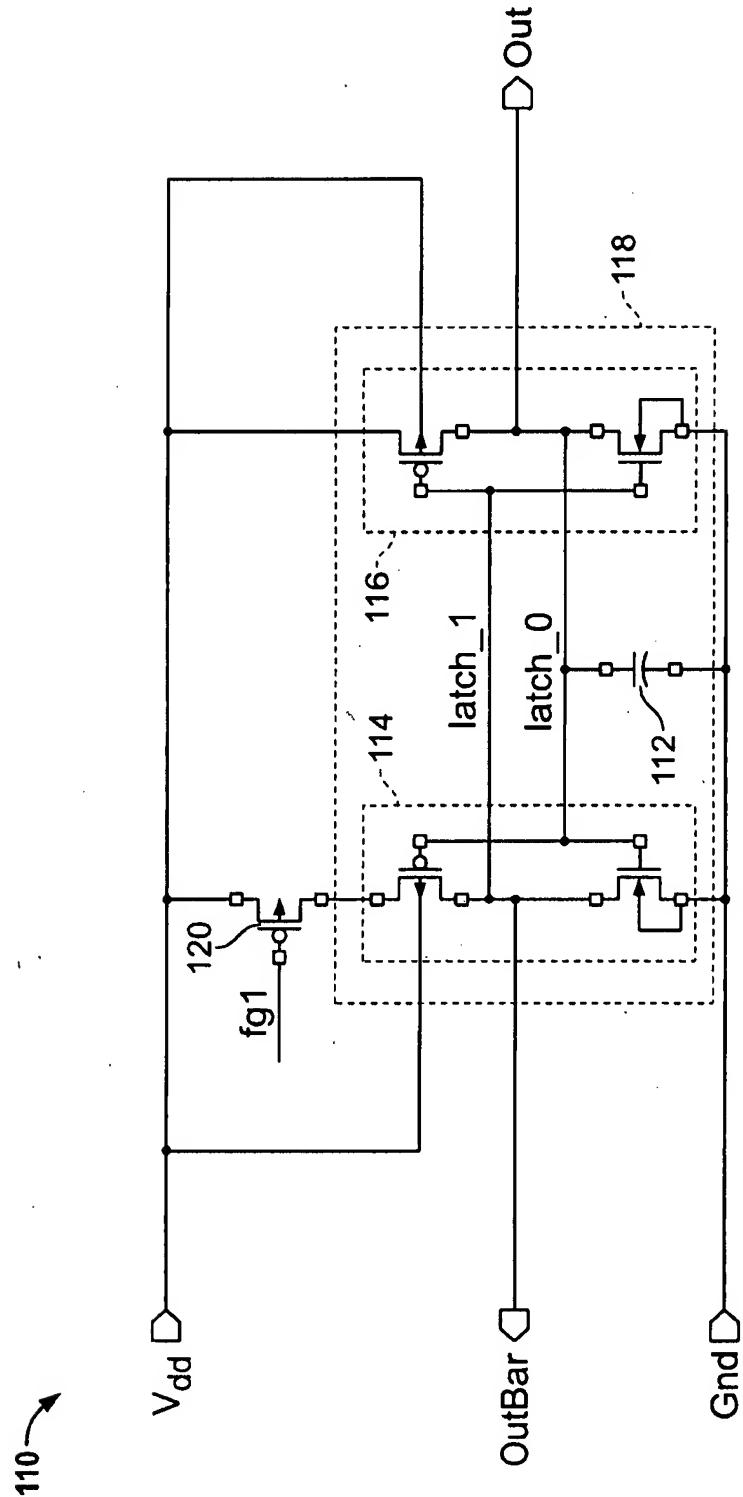


FIG. 11

REPLACEMENT SHEET

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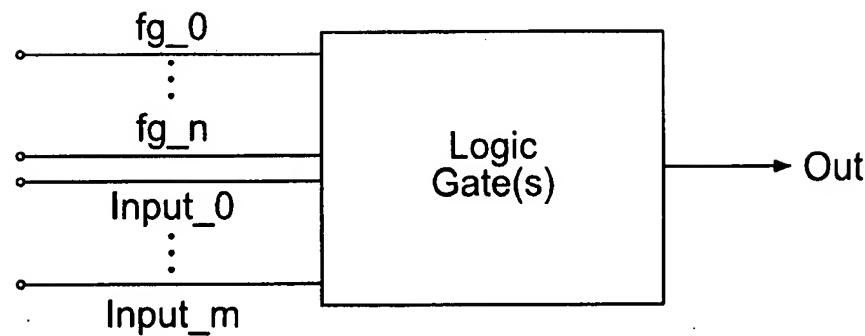


FIG. 12A

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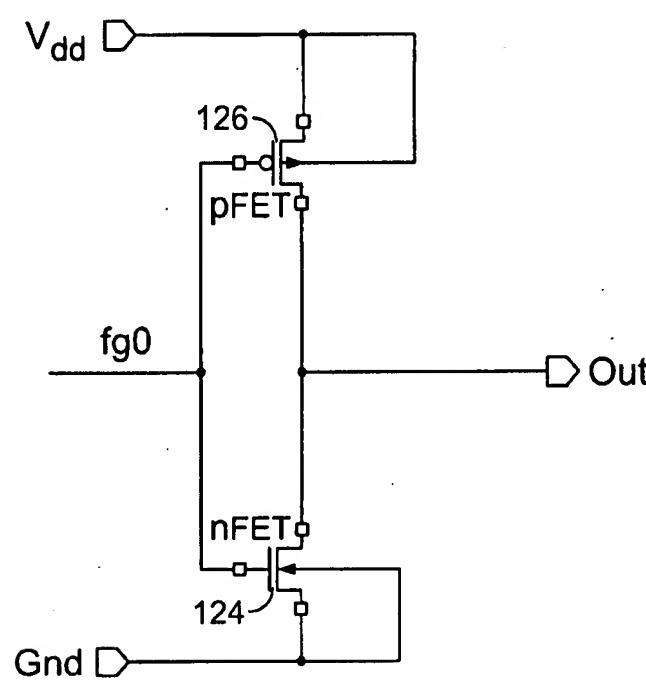


FIG. 12B

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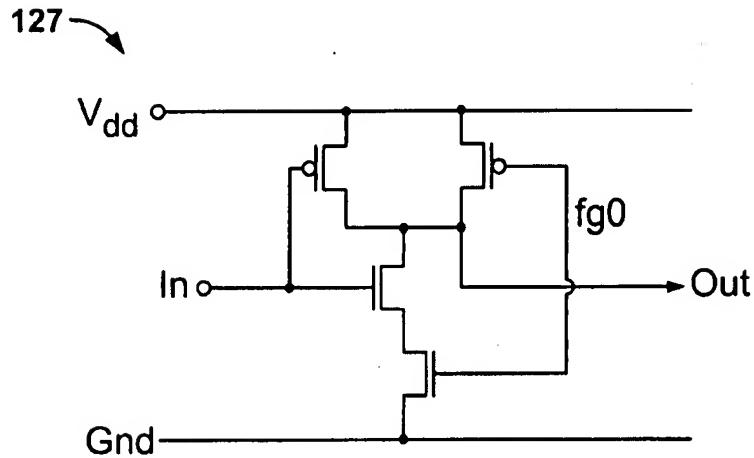


FIG. 12C

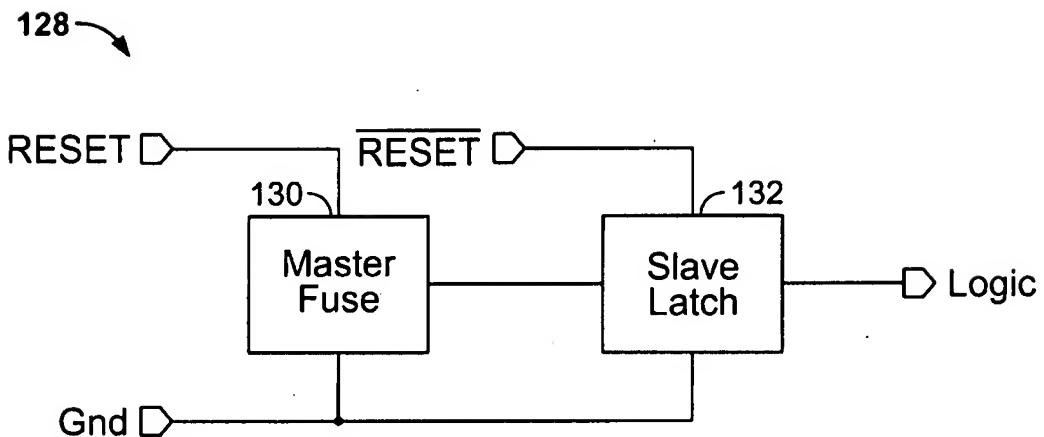


FIG. 13

REPLACEMENT SHEET

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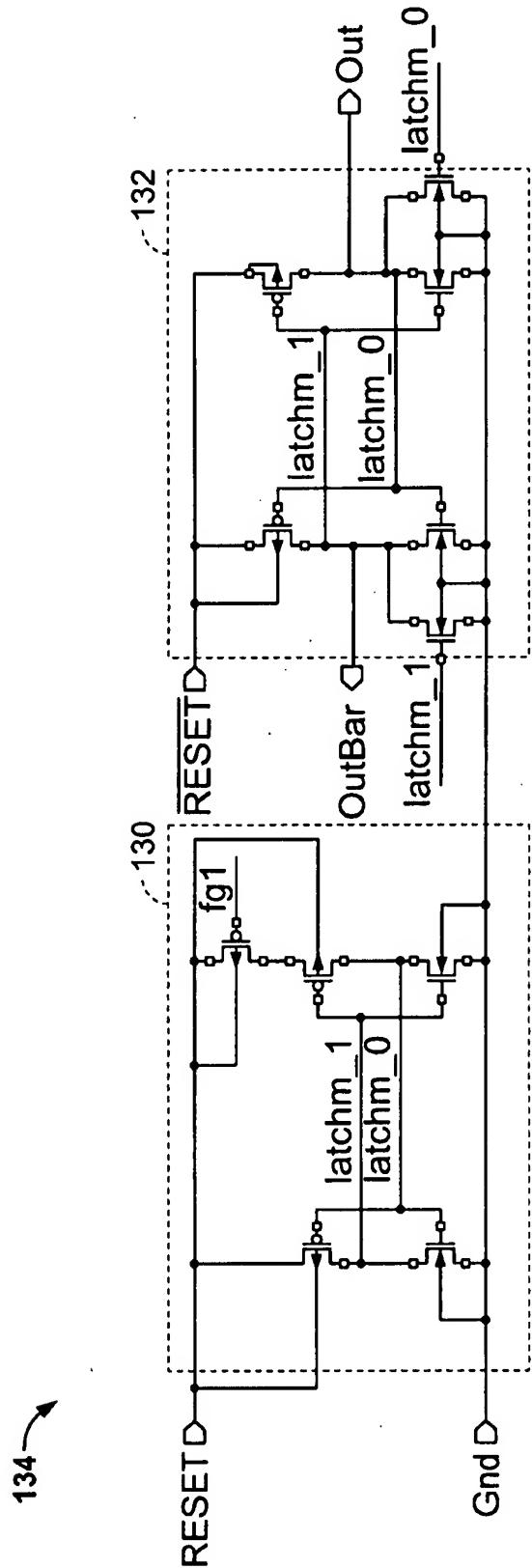


FIG. 14

REPLACEMENT SHEET

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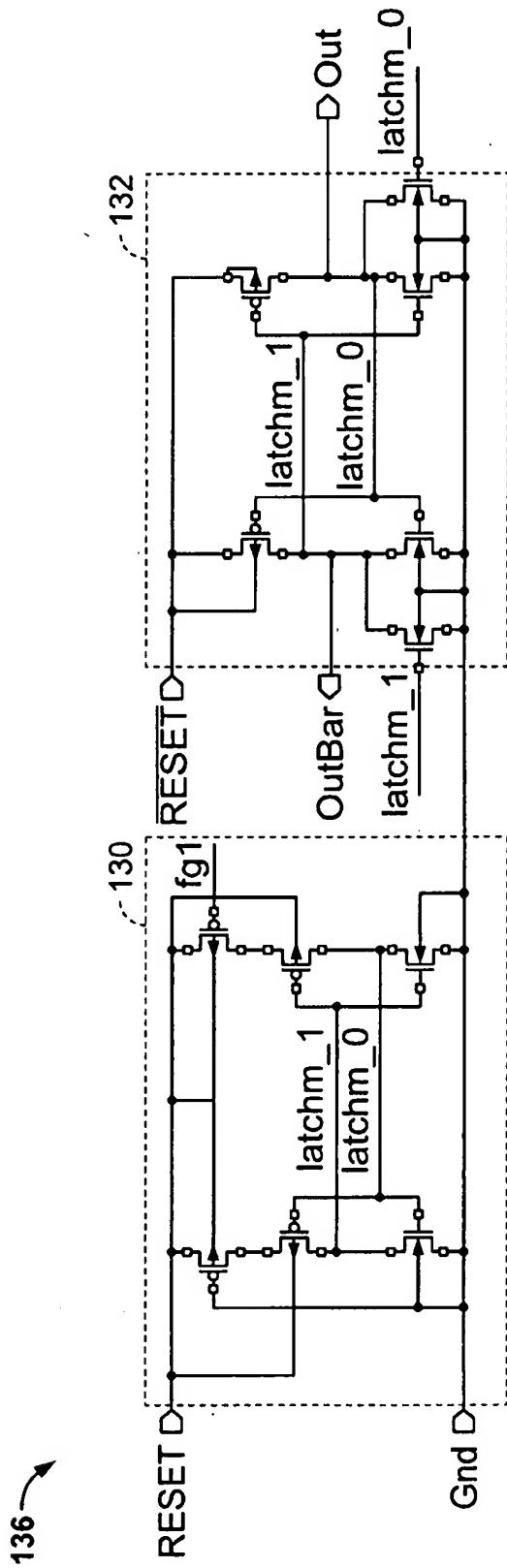


FIG. 15

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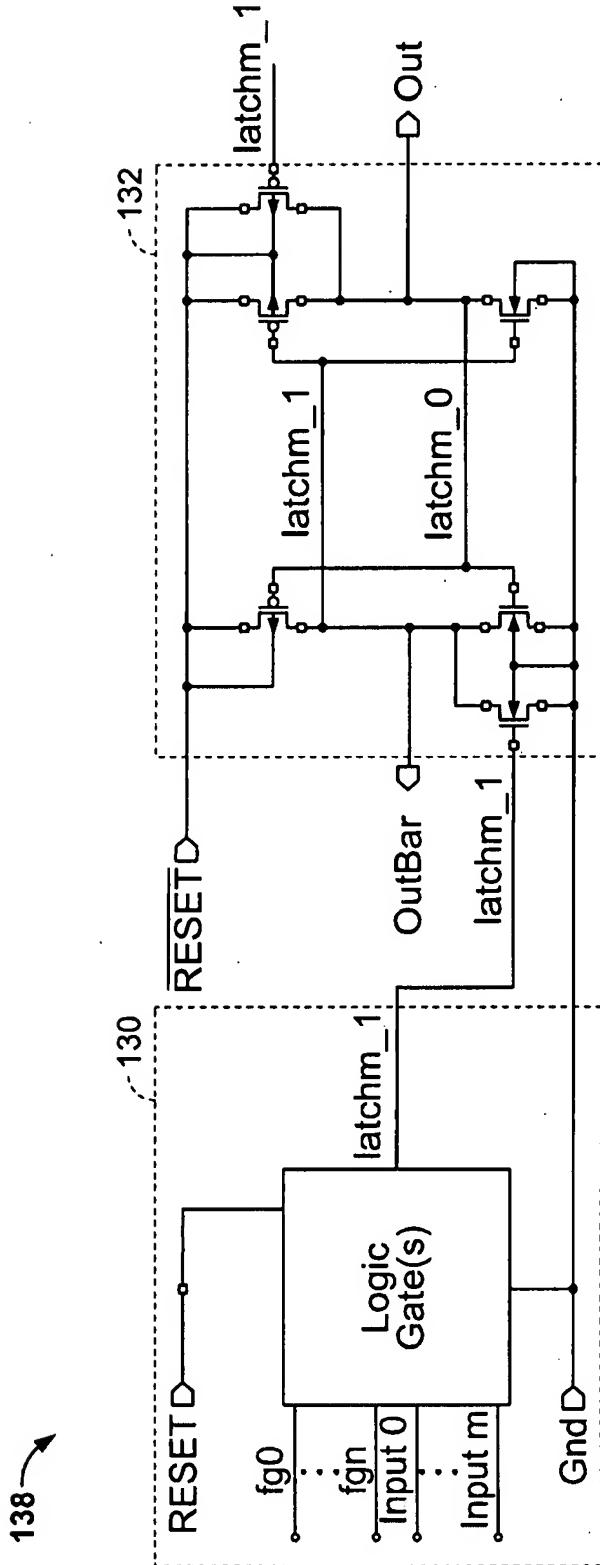


FIG. 16

REPLACEMENT SHEET

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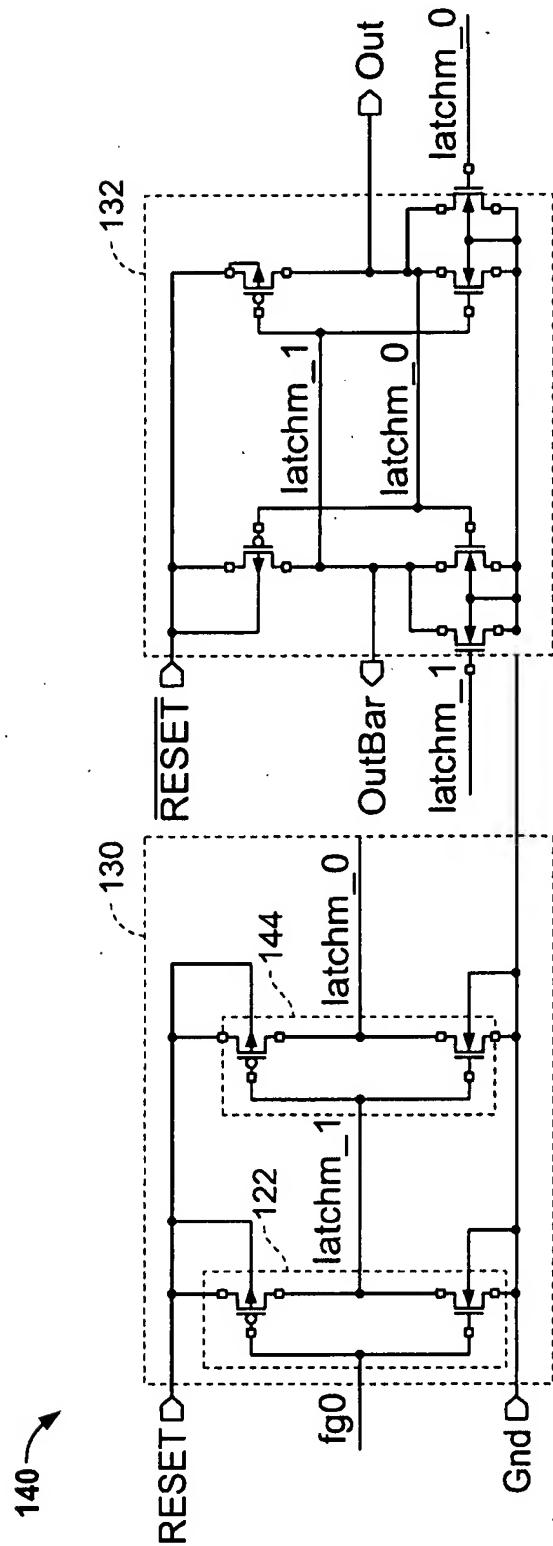


FIG. 17

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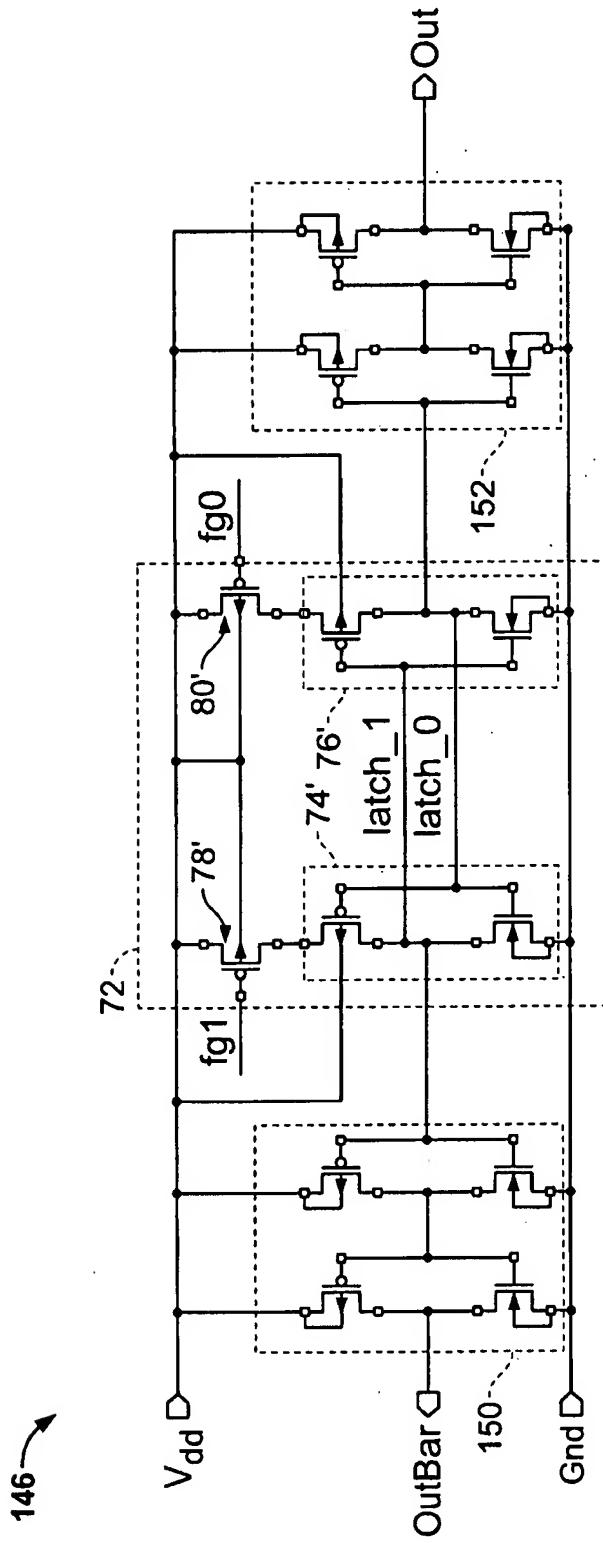


FIG. 18

REPLACEMENT SHEET

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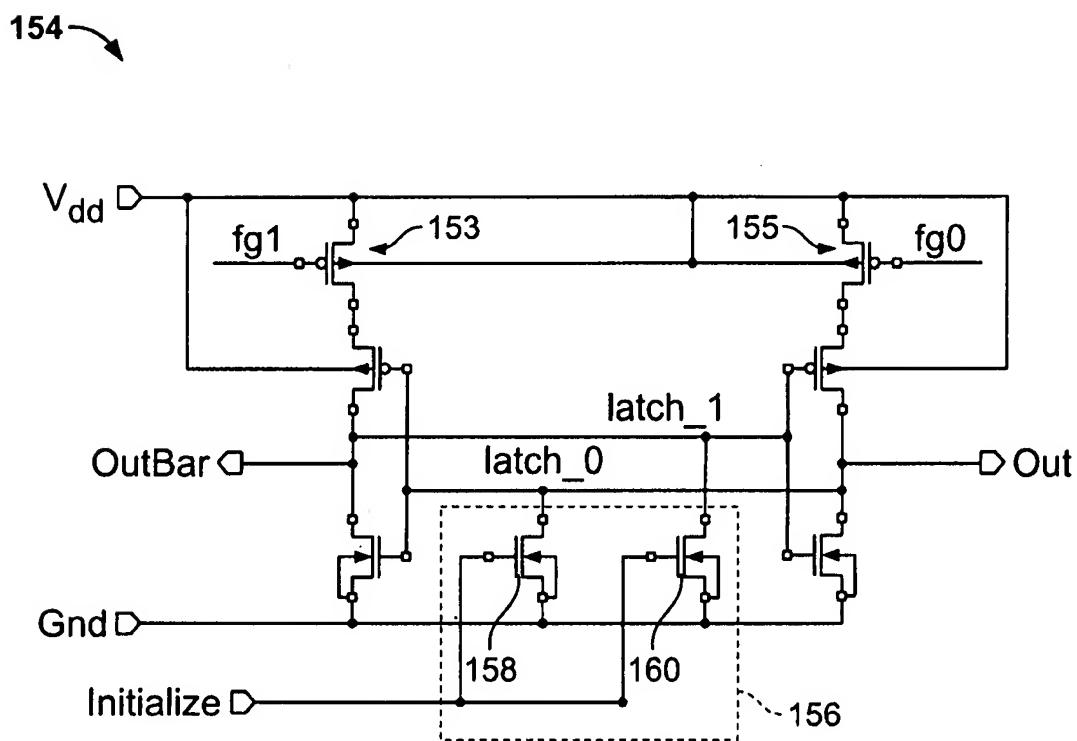


FIG. 19

REPLACEMENT SHEET

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MOSCAP Tunneling Junction

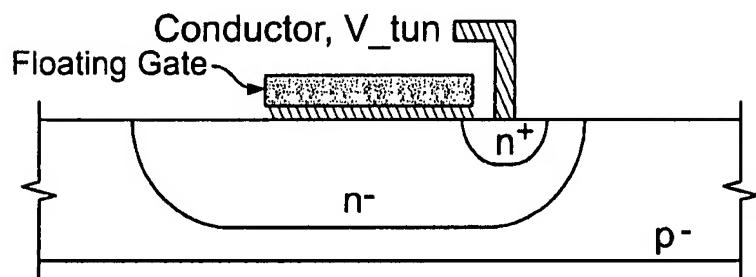


FIG. 20

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pFET Tunneling Junction

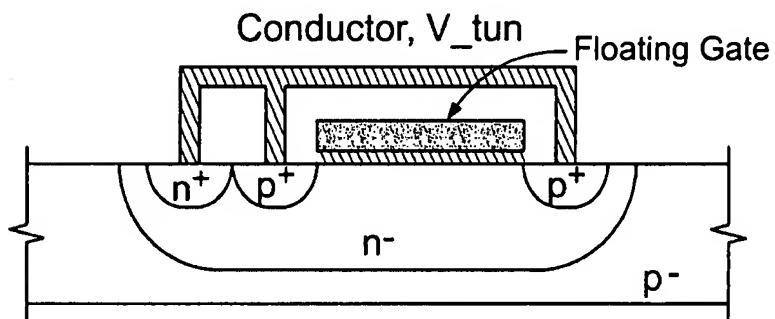


FIG. 21

REPLACEMENT SHEET

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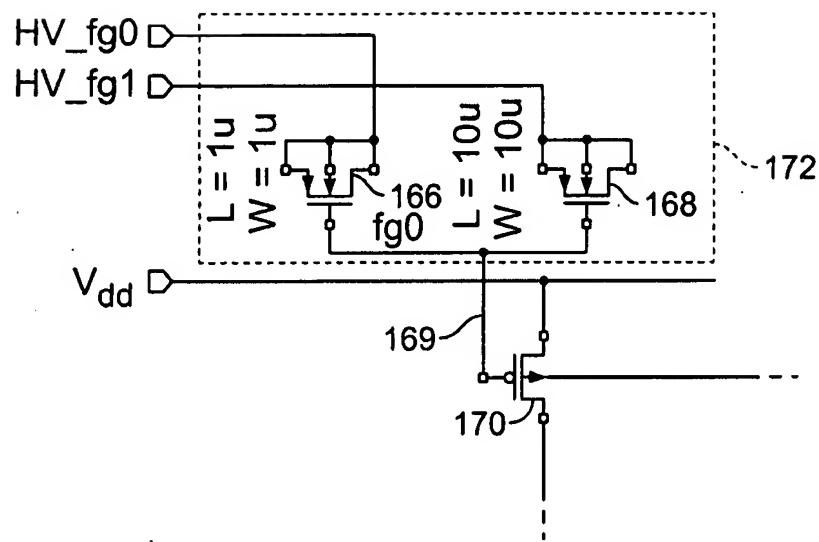


FIG. 22

REPLACEMENT SHEET

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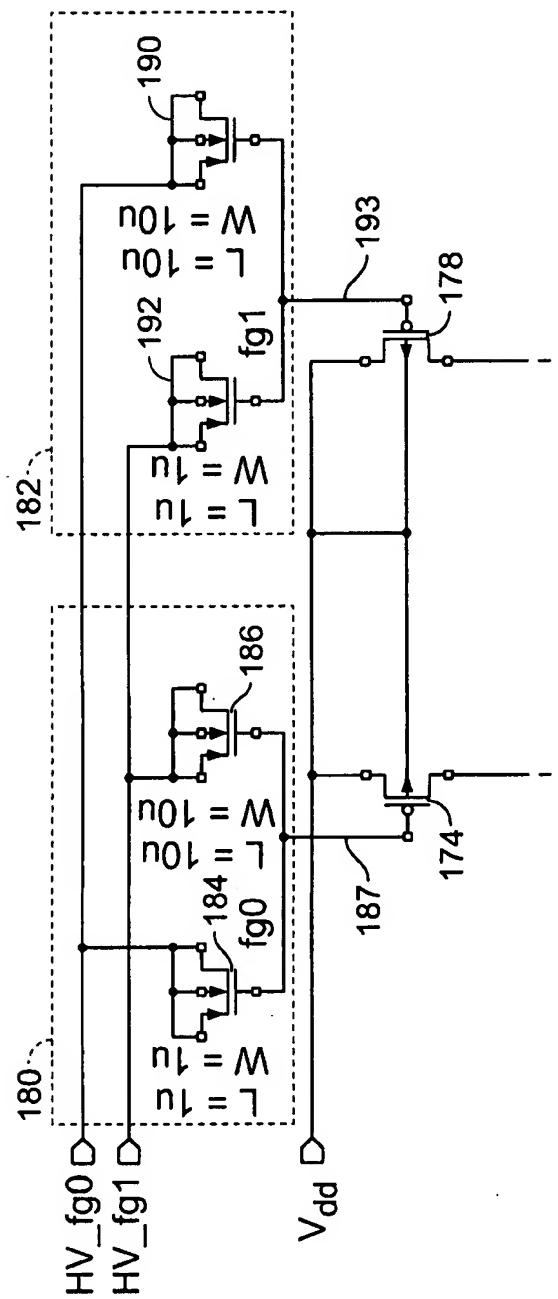


FIG. 23

REPLACEMENT SHEET

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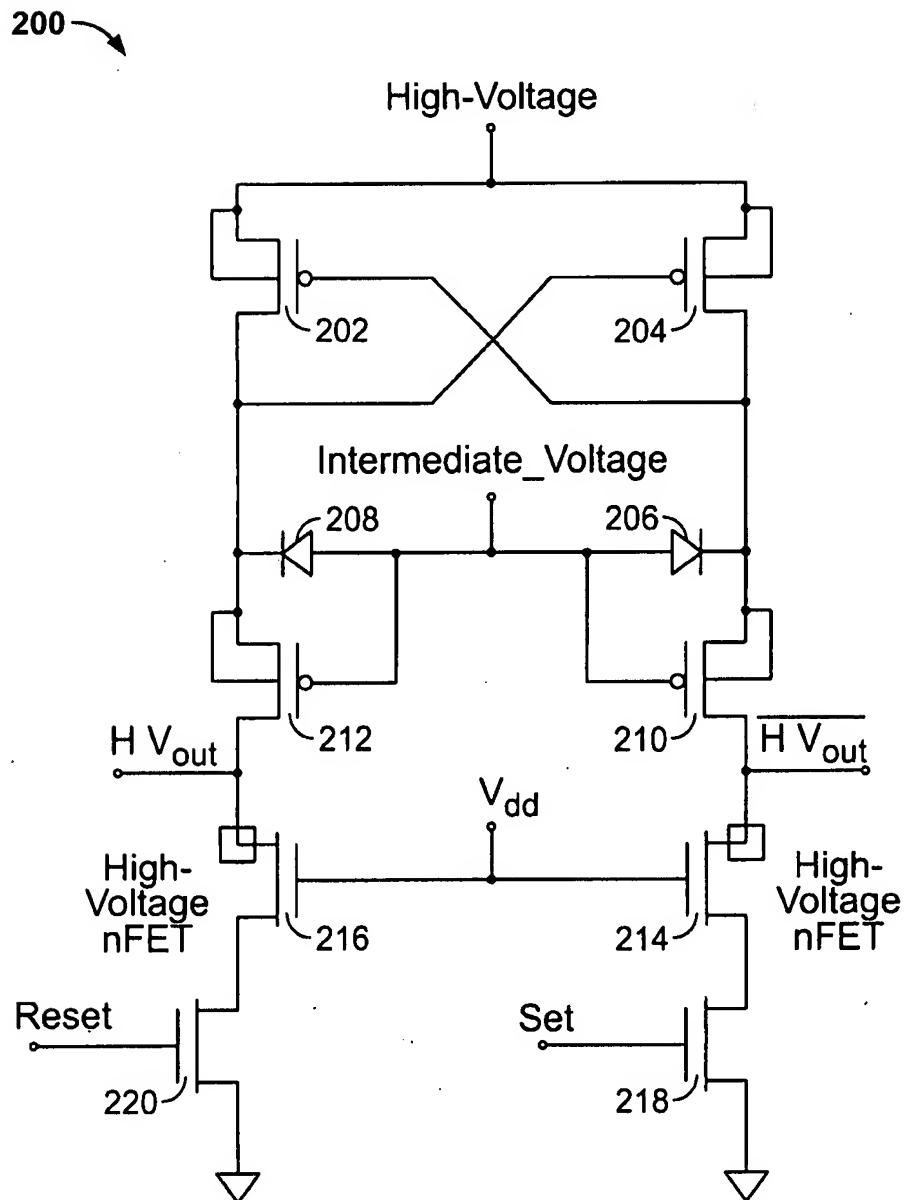


FIG. 24

REPLACEMENT SHEET

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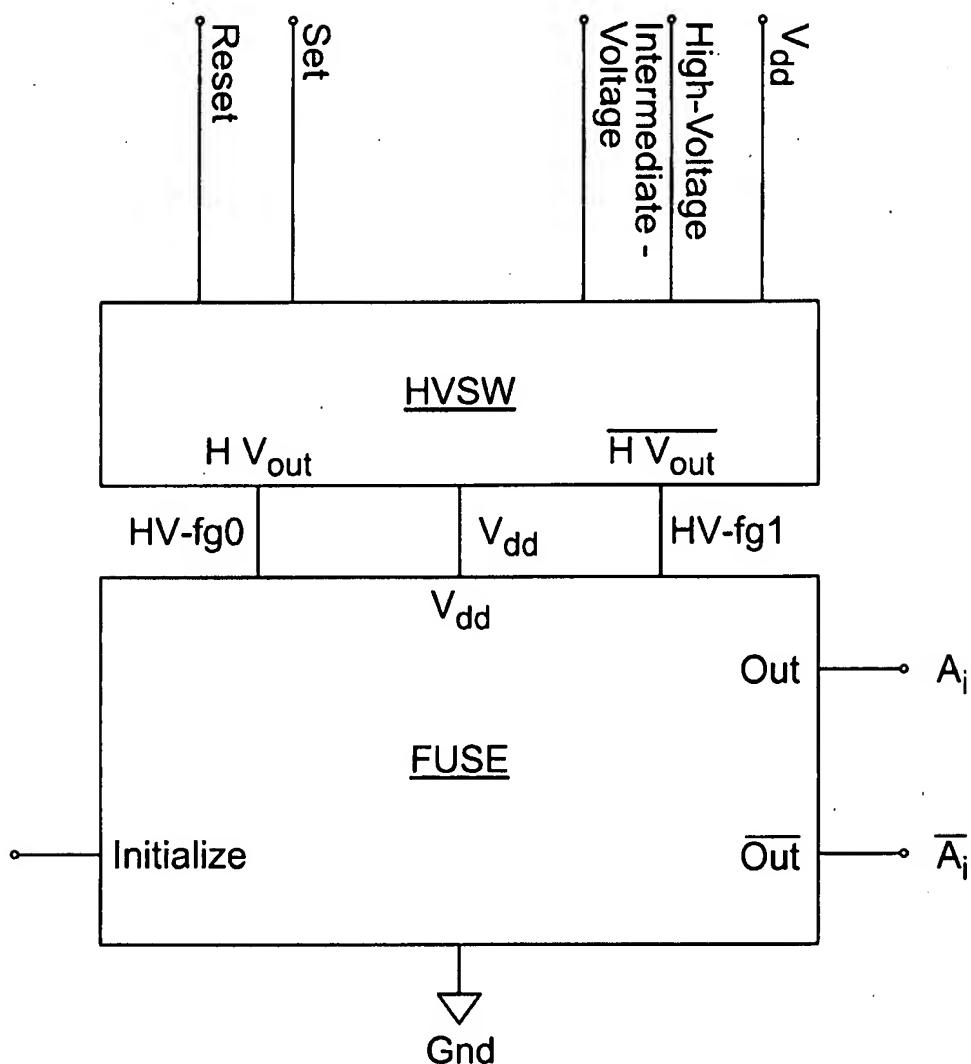


FIG. 25

REPLACEMENT SHEET

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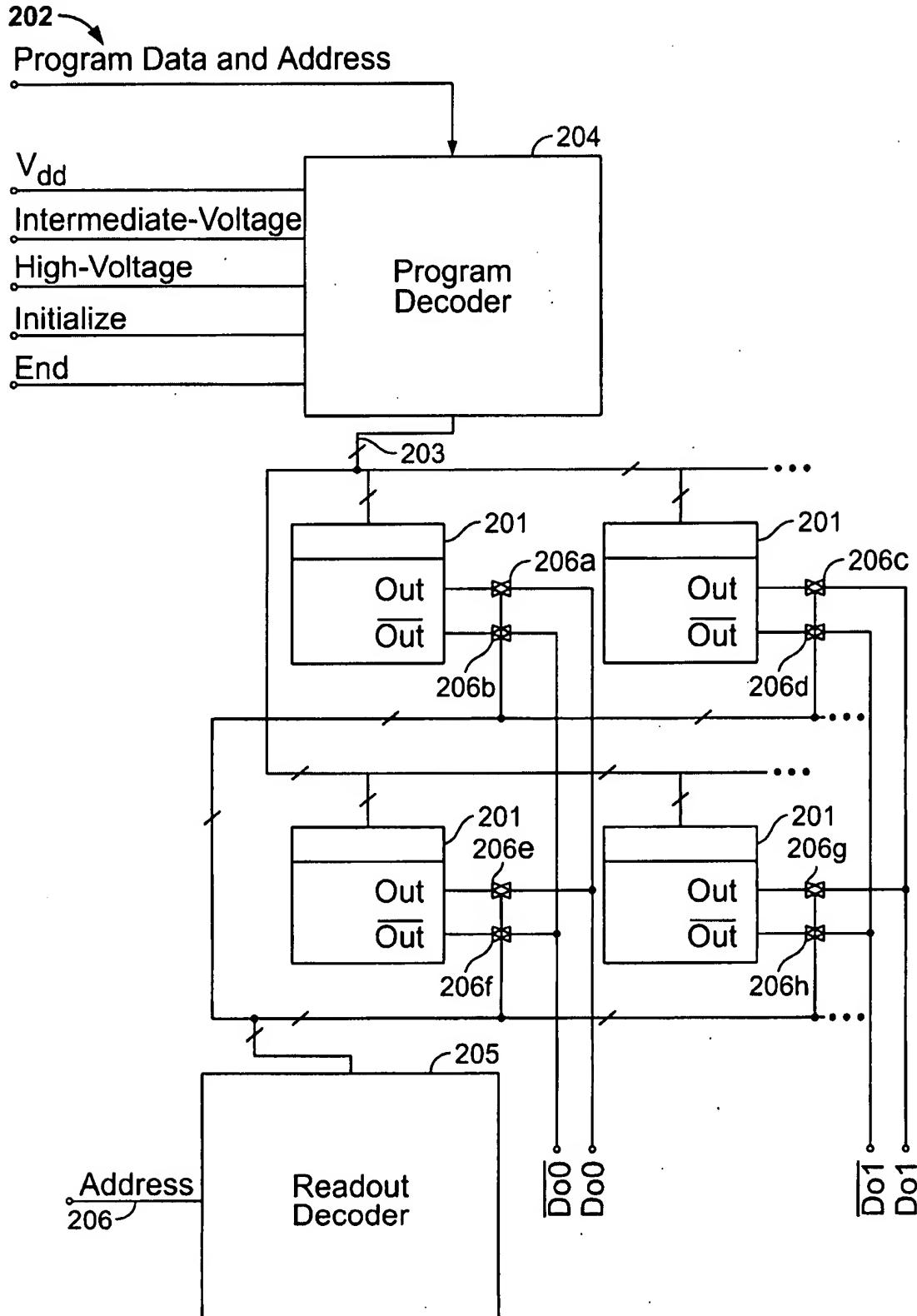


FIG. 26

REPLACEMENT SHEET

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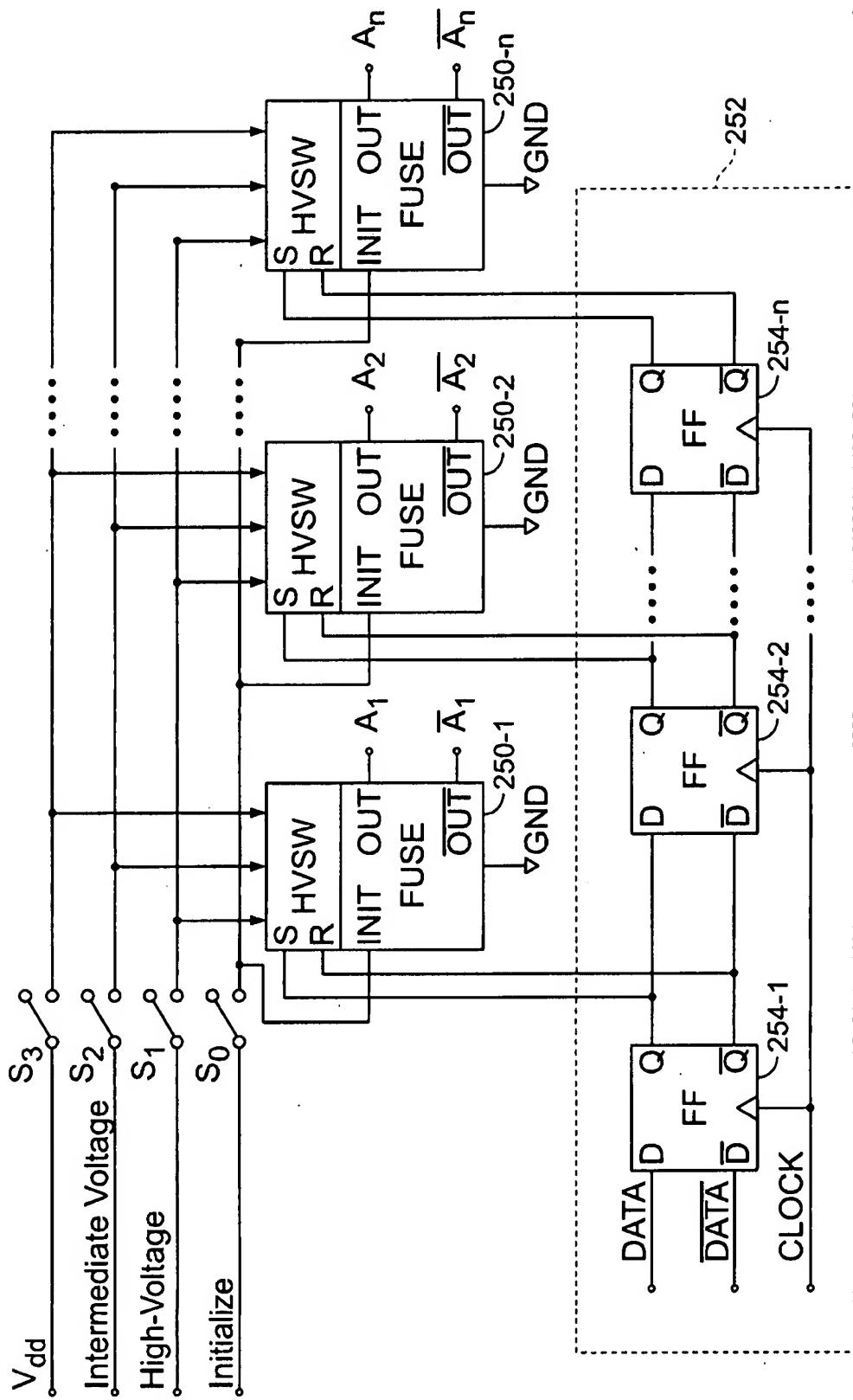


FIG. 27

REPLACEMENT SHEET

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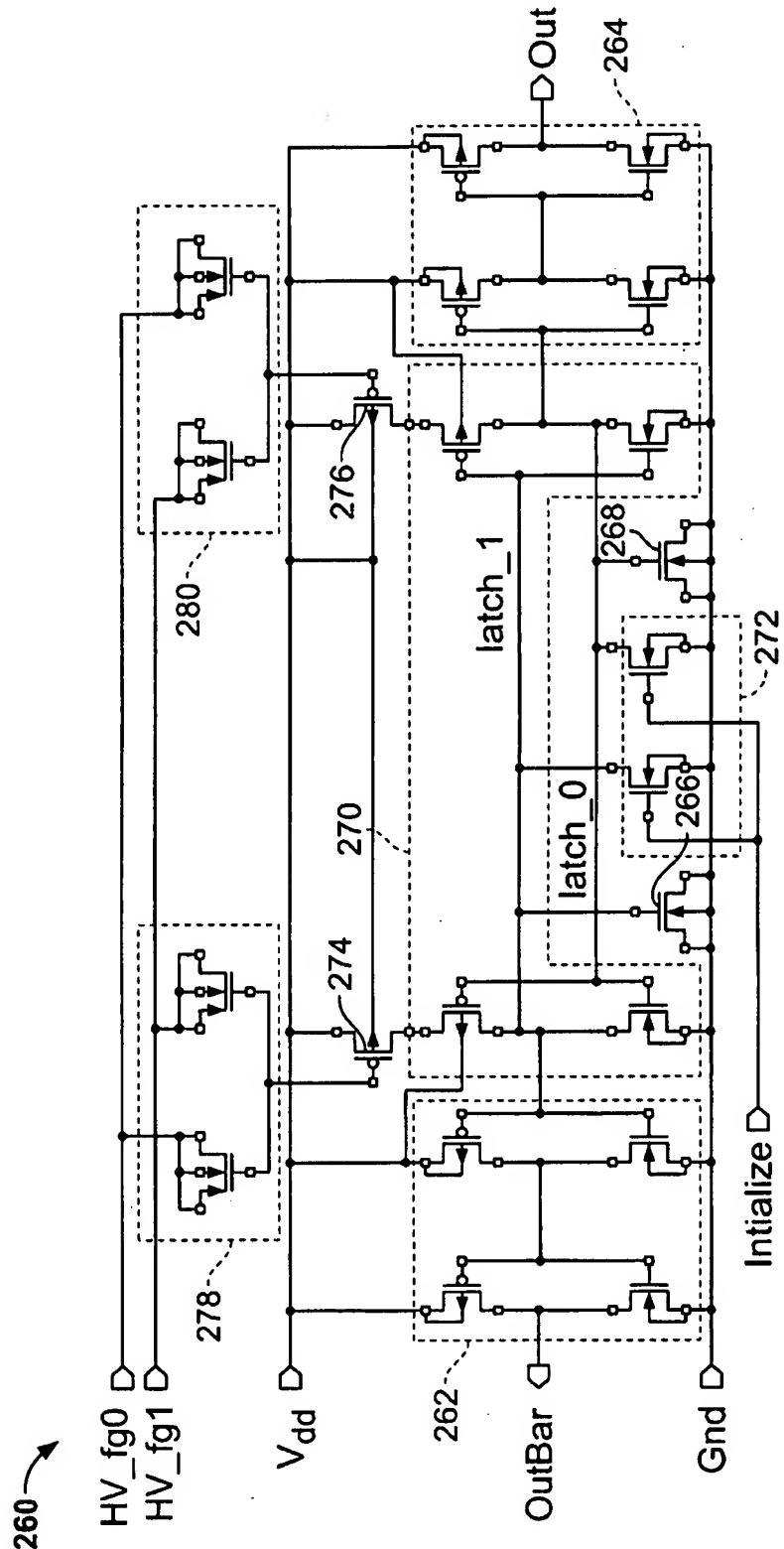


FIG. 28

REPLACEMENT SHEET

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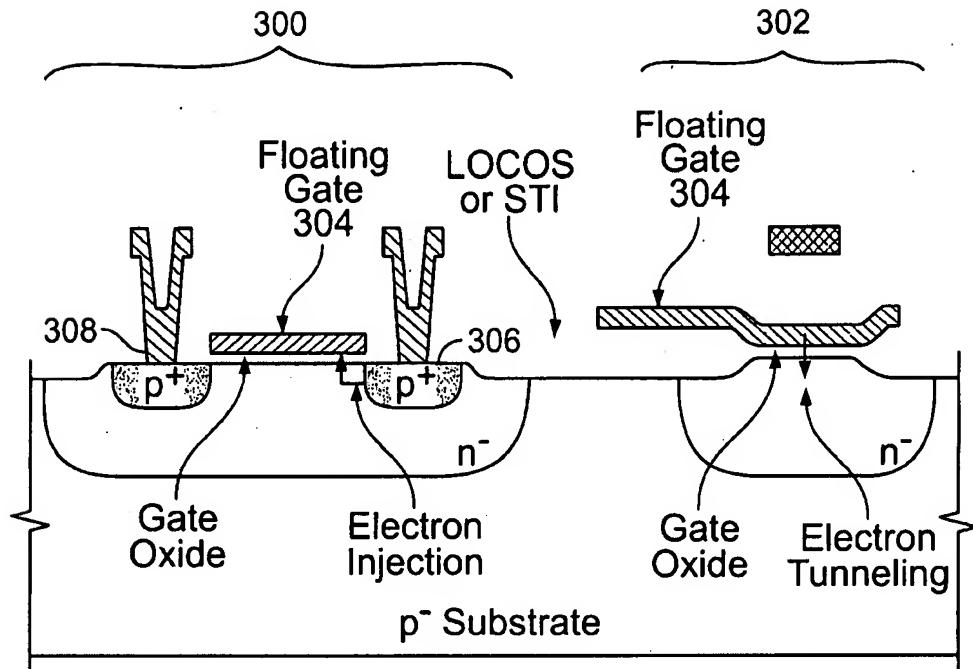


FIG. 29A

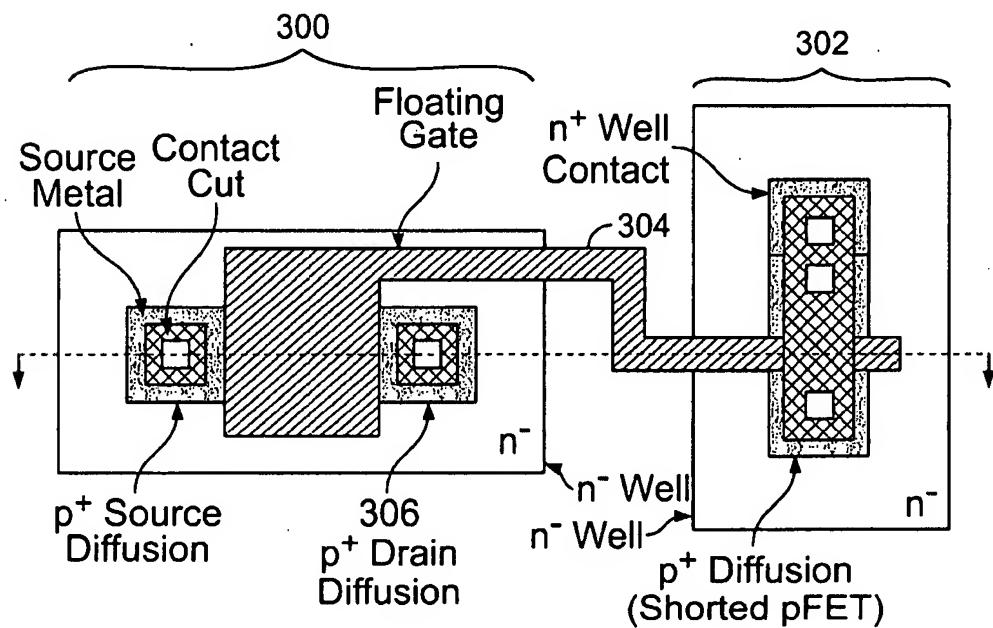


FIG. 29B